

REASONS FOR ALLOWANCE

The following is an examiner's statement of reasons for allowance:

With regards to claims 1-6,8-12, 14-17, 19, and 20, the prior art of record does not disclose or fairly teach the specific circuit configuration with emphasis on the a third metal layer on a first surface of the first laminate layer and patterned to form a first capacitor plate; and a fourth metal layer on a second surface of the first laminate layer opposite the first surface and patterned to form a second capacitor plate, wherein the first capacitor plate and second capacitor plate form a parallel plate capacitor.

With regards to claims 21-24, the prior art of record does not disclose or fairly teach the specific circuit configuration with emphasis on a second LCP layer and a first outer organic layer on the second prepreg layer opposite the second metal layer and a second outer organic layer on the third prepreg layer opposite the third metal layer.

With regards to claims 25-36, the prior art of record does not disclose or fairly teach the specific circuit configuration with emphasis on a second patterned metal layer on the second surface of the LCP layer, wherein the first and second metal layers are patterned to form at least a first resonator, a second resonator, a third resonator, and a feedback capacitor, the third resonator connecting the first and second resonators, and the feedback capacitor connected in parallel with the third resonator.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably

accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to KIMBERLY E. GLENN whose telephone number is (571)272-1761. The examiner can normally be reached on Monday-Friday 7:30 to 4:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal can be reached on (571)-272-1769. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Kimberly E Glenn
Examiner
Art Unit 2817

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/K. E. G./
Examiner, Art Unit 2817

/Robert Pascal/

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Supervisory Patent Examiner, Art Unit 2817